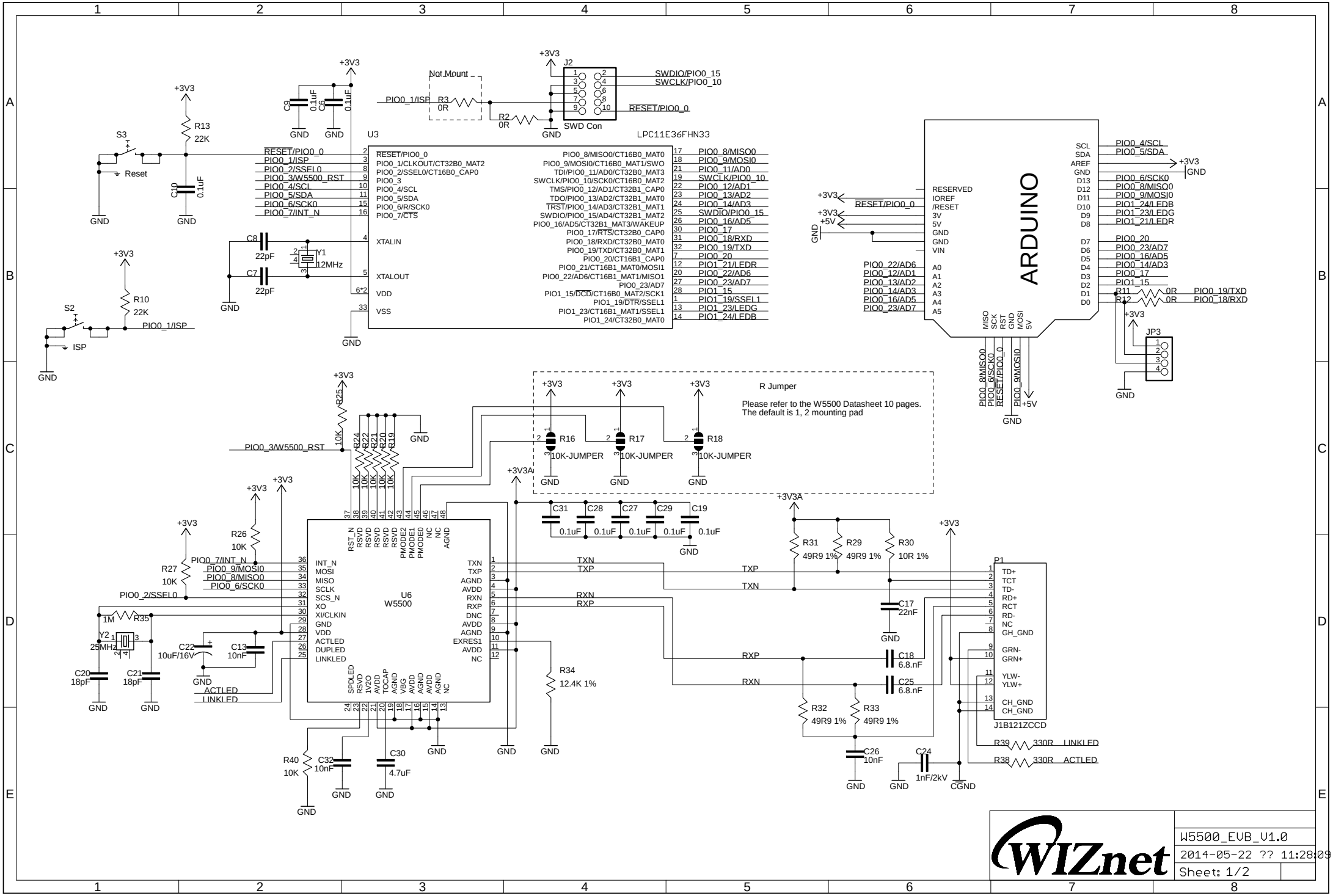
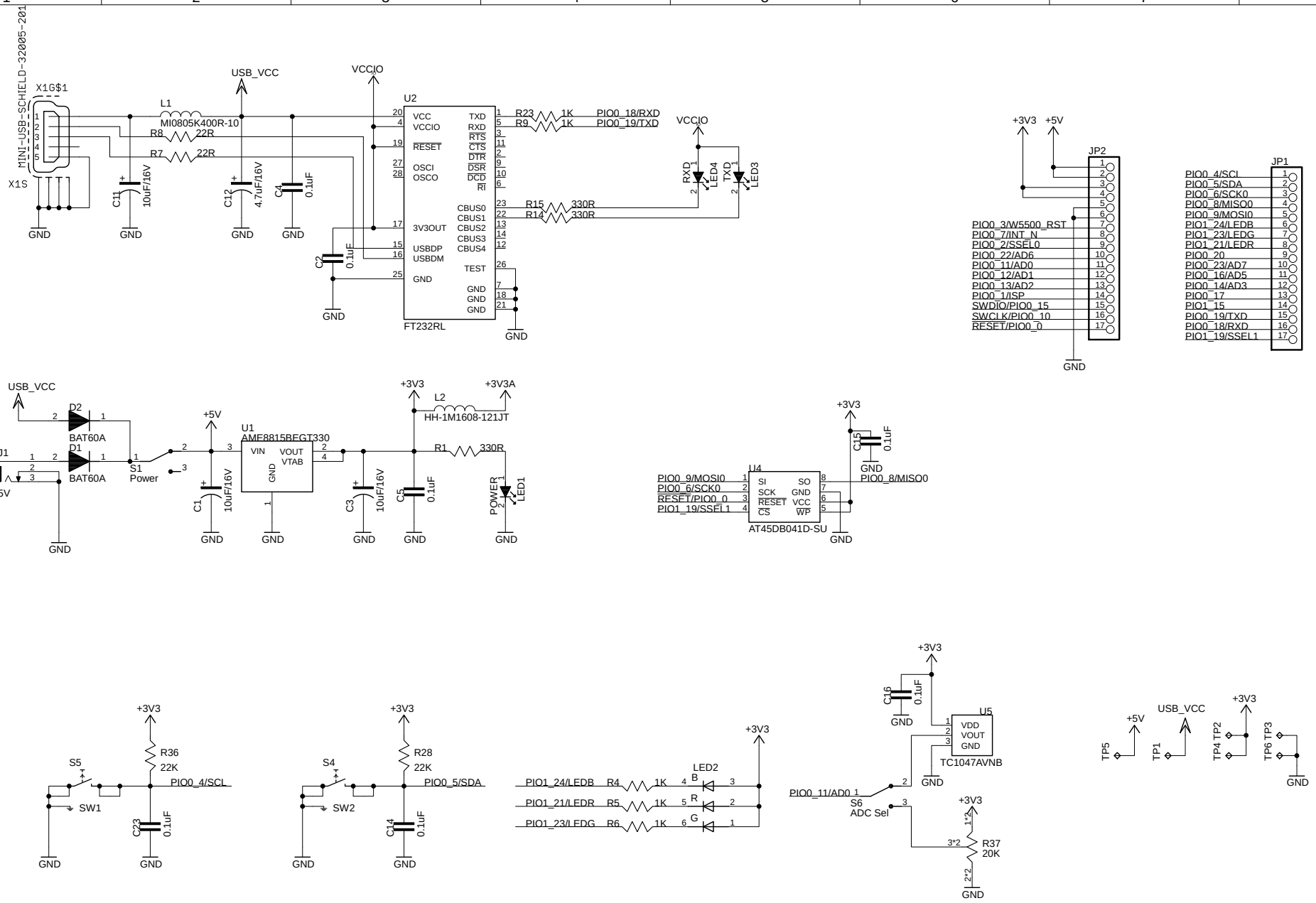

U3
LPC11E36FHN33
J2
SWD Con
Not Mount
R3 0R
R2 0R
PIO0_1/ISP
SWDIO/PIO0_15
SWCLK/PIO0_10
RESET/PIO0_0
S3
Reset
S2
ISP
R13 22K
R10 22K
C10 0.1uF
C9 0.1uF
C6 0.1uF
C8 22pF
C7 22pF
Y1 12MHz
XTALIN
XTALOUT
VDD
VSS
ARDUINO
JP3
R11 0R
R12 0R
PIO0_19/TXD
PIO0_18/RXD
R25 10K
PIO0_3/W5500_RST
R24 R22 R21 R20 R19 10K
R16 10K-JUMPER
R17 10K-JUMPER
R18 10K-JUMPER
R Jumper
Please refer to the W5500 Datasheet 10 pages.
The default is 1, 2 mounting pad
U6
W5500
R26 10K
R27 10K
R35 1M
Y2 25MHz
C20 18pF
C21 18pF
C22 10uF/16V
C13 10nF
ACTLED
LINKLED
R34 12.4K 1%
R40 10K
C32 10nF
C30 4.7uF
C31 C28 C27 C29 C19 0.1uF
R31 49R9 1%
R29 49R9 1%
R30 10R 1%
C17 22nF
C18 6.8nF
C25 6.8nF
R32 49R9 1%
R33 49R9 1%
C26 10nF
C24 1nF/2kV
P1
J1B121ZCCD
R39 330R LINKLED
R38 330R ACTLED
TXN
TXP
RXN
RXP
+3V3
+3V3A
+5V
GND
CGND
WIZnet
W5500_EVB_V1.0
2014-05-22 ?? 11:28:09
Sheet: 1/2



WIZnet	W5500_EVB_V1.0
	2014-05-22 ?? 11:28:09
	Sheet: 2/2