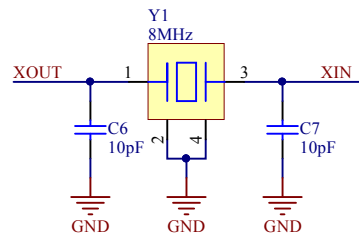
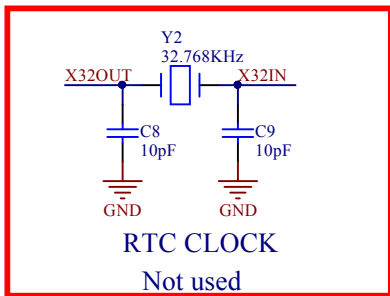


ISP UART

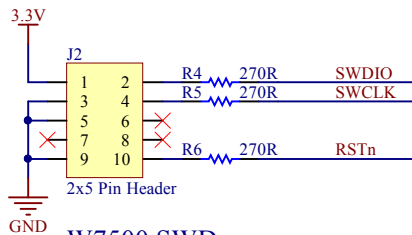


MAIN CLOCK

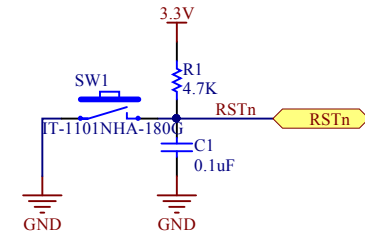
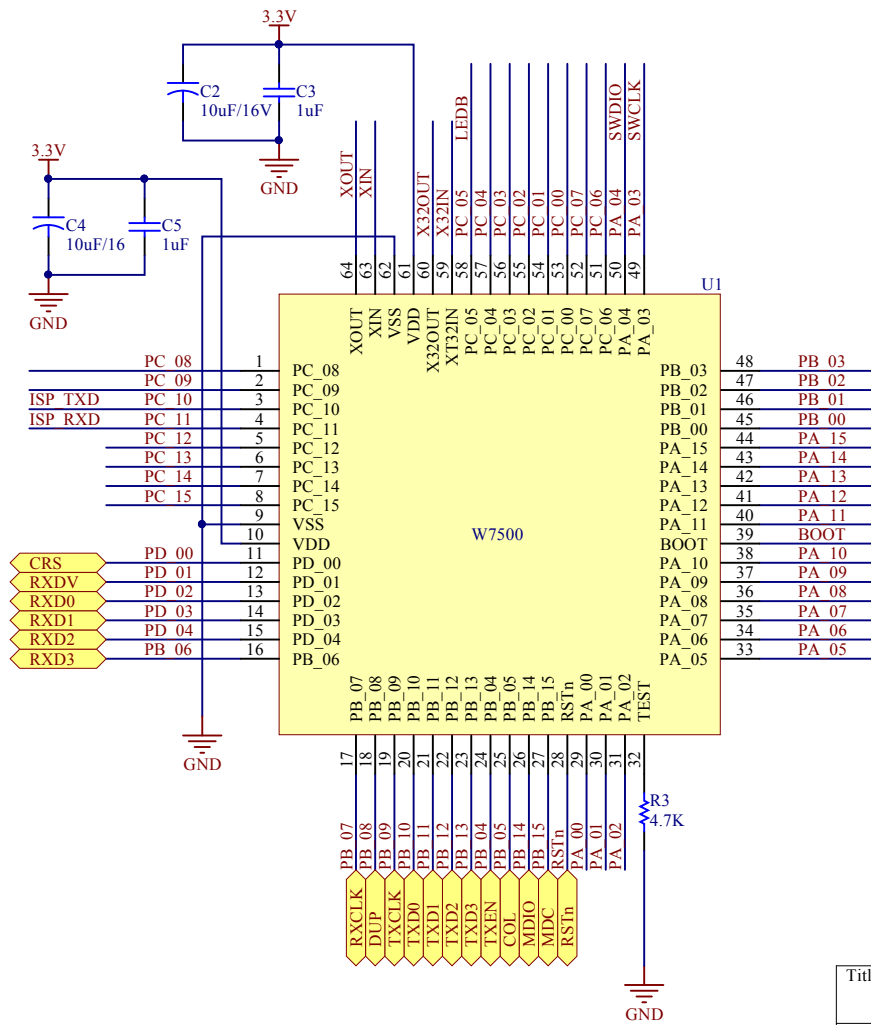


RTC CLOCK

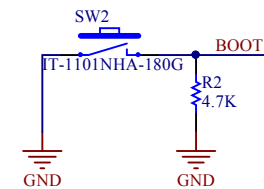
Not used



W7500 SWD

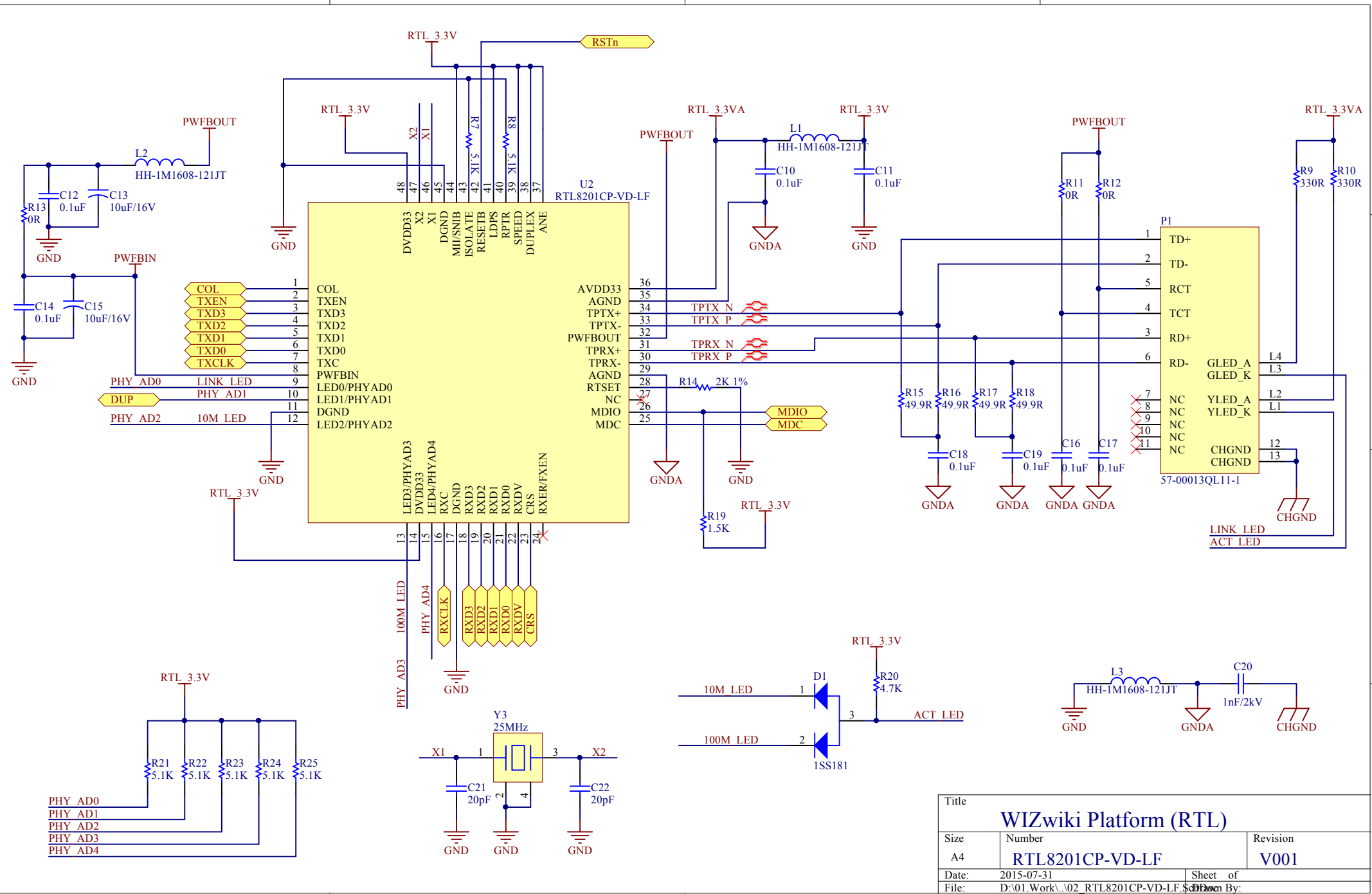


RESET SW



BOOT SW

Title		
WIZwiki Platform (RTL)		
Size	Number	Revision
A4	W7500	V001
Date:	2015-07-31	Sheet of
File:	D:\01.Work\...\01_W7500.SchDoc	Drawn By:



Title		
<b>WIZwiki Platform (RTL)</b>		
Size	Number	Revision
A4	RTL8201CP-VD-LF	V001
Date:	2015-07-31	Sheet of
File:	D:\01.Work\...02_RTL8201CP-VD-LF.SchDoc	Drawn By: