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Overview

W5100S-EVB support DMA(Direct Memory Addressing) of SPI Mode. To operate at its maximum speed, the SPI needs to be fed with the data for transmission and the data received on the Rx buffer should be read to avoid overrun.

Development Environment

- MCU : STM32F103VC
 - System Clock : 72MHz, SPI2 clock : 18MHz(Max clock)
- Used program: Atollic True STUDIO
- W5100S Setting
 - Socket size : 8K, No Delay Ack

Application note

Revision history

Version	Date	Download
1.0.0	2018-12-14	W5100S_AN_DMA_V100K.pdf
	2019-04-16	W5100S_AN_DMA_V100E.pdf

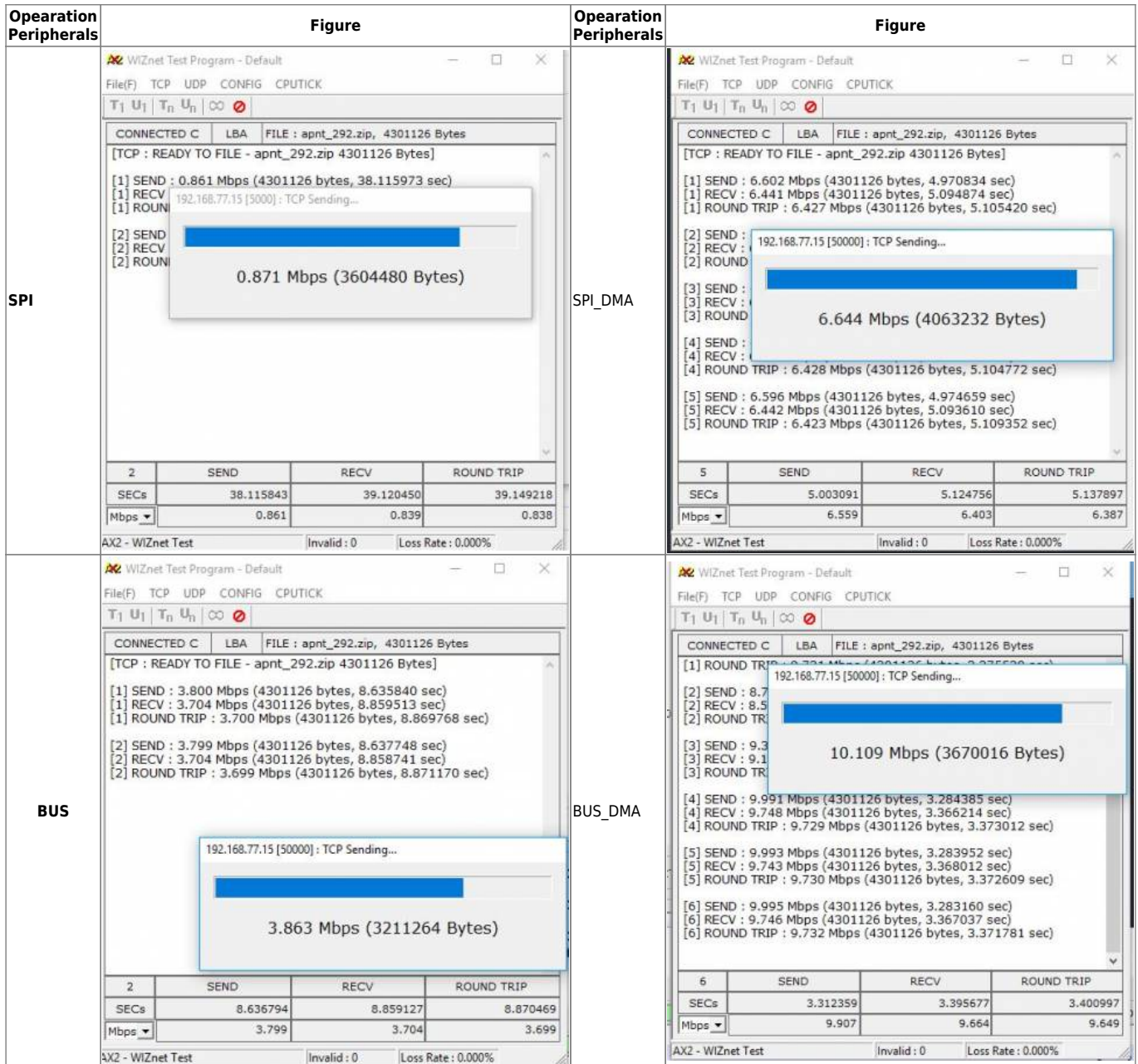
Reference Code

Note : DMA reference code is designed for W5100S. But if you use the MCU of STM32F1xx,the other chips(W5100, W5100S, W5200, W5300, W5500) can use this application.

Revision history

Version	Date	Download	ETC
1.0.0	2018-12-14	W5100S-EVB	Initial Version

Comparison table



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