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Overview

W5100S provides **INTn(Interrupt Pin)** and HOST is notified when an Ethernet Communication Event has occurred through **INTn**. When an Ethernet Communication Process Event (IP Collision, WOL Packet Reception, Data Transmission, Reception for each SOCKET, etc.) occurs, the **INTn** is asserted low

Development Environment

- MCU :
- Used program: -

Application note

Revision history

Version	Date	Download
1.0.0	2018-04-01	W5100S_AN_Interrupt_V110K.pdf W5100S_AN_Interrupt_V100E.pdf

Reference Code

Revision history

Version	Date	Download	ETC
1.0.0		not_yet	Initial Version

For more information [W5100s](#) chip please also refer to the chip's datasheet:

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